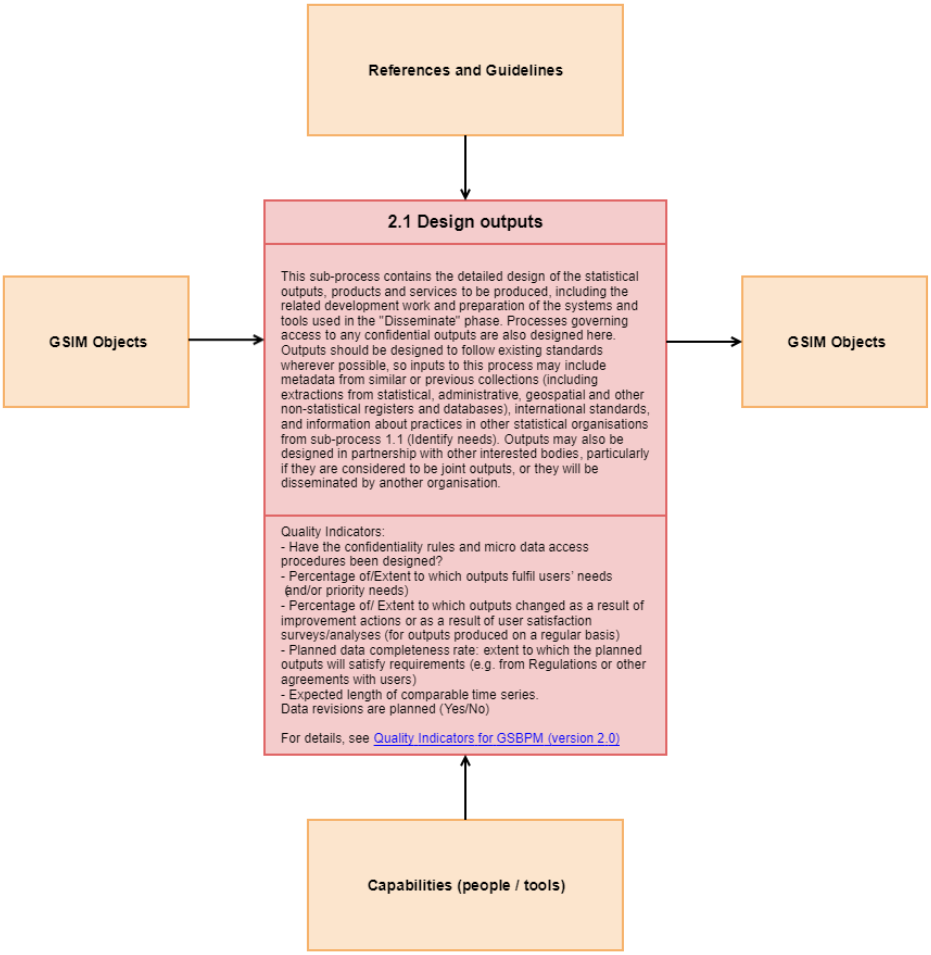2.1 Design outputs